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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/072,346

02/06/2002

Paul William Coteus

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5924

7590

02/24/2005

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EXAMINER

NGUYEN, VIET Q

ART UNIT

PAPER NUMBER

2827

DATE MAILED: 02/24/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/072,346	Applicant(s) COTEUS ET AL.	
	Examiner Viet Q. Nguyen	Art Unit 2827	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 1 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-37 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☐ Claim(s) ____ is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☒ Claim(s) 1-37 are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Election/Restrictions

1. This application contains claims directed to the following patentably distinct species of the claimed invention:

Group 1, claims **1-6 and 21-26** are drawn to a memory assembly having a selective capability of directing address information portion appearing in a adapt path to the plurality of addressable storage locations in a separate processing path without disturbing said information stored in the plurality of storage locations”;

Group 2, claims **7-10 and 27-30** are drawn to a RAM memory with a method of verifying that the location to which a specific data increment was directed is the location in which it resides comprising the steps of “directing the address information... through a separate path... to an output location”, “providing at said output location access in the register to the assigned location in which data increment was to be stored”, and “comparing data in said separate path with said register for a different storage location”;

Group 3, claims **11-12 and 31-32** are drawn a RAM memory with a method of tuning the timing of said RAM for optimization comprising the steps of “directing the address information... through a separate path...to an output location”, “providing at said output location separately stored increments said clock function and separately stored address portions of said data”, and “comparing data in said separately stored address portions with a corresponding pulse from said clock function and identifying

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events where said clock function pulse occurred other than during said address portion of said data path with said register for a difference of storage location”;

Group 4, claims **13-14** are drawn to a RAM memory having plurality of array banks... with the improvement for calibration of the impedance of the driver elements comprising means for “providing, during a write command cycle, an adjust signal operable to disable input from the common data bus into the array bank, and to disconnect the write command signal from circuitry of said columns, and means for delivering impedance control vector signals, indicating at least one change of magnitude and of satisfaction with the present impedance state, to each power driving elements, and means for producing impedance control instructions on said common data bus, etc.;

Group 5, claim **15** is drawn to a RAM memory having plurality of array banks...with the improvement for calibration of impedance of driver elements comprising means for providing, during a write command cycle, adjust up and down signals operable to provide a clocked latched and decoded input to each said driver element as impedance control vector signal, indicating at least one of change of magnitude and of satisfaction with the present impedance state, to each of said power driving elements”;

Group 6, claims **16-17 and 33-34** are drawn to a RAM memory having plurality of array banks... with the improvement for adjustment of timing skews comprising means for placing into the common data bus, in one read cycle, during the duration of a column

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access time, a uniform set of serial signals, and means for holding enablement of delivery of said uniform set of serial signals until the start of access of a column;

Group 7, claims **18-20 and 35-37** are drawn to a RAM memory having an input port and an output port and plurality of drivers driving through a common communication channel data path with the improvement comprising means for providing separate read and write cycles and means, taking place during said read cycle, for redirection of address data, from said data path to said array banks, to a data path to at least said output port.

Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable. Currently, no claim is generic.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

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Should applicant traverse on the ground that the species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the species to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103(a) of the other invention.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Viet Q. Nguyen whose telephone number is (571) 272-1788. The examiner can normally be reached on 7am-6pm (EST).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoai Ho can be reached on (703) 308-4910. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Viet Q Nguyen
Primary Examiner
Art Unit 2827



V. Nguyen,
